

**Amendments to the Specification:**

Please replace the paragraph beginning at page 2, line 8, with the following amended paragraph:

For the above reason, a display device as shown in FIG. 2 in which a charge pump circuit is configured on a substrate to supply voltages required for driving was developed. FIG. 2 is an outline view of the periphery of a display device of portable information equipment having a conventional charge pump. A pixel portion 204, a source signal line driving circuit 202, a gate signal line driving circuit 203, a switching element 205 are integrally formed on a substrate 201. Capacitors 207 and 208 are loaded on an FPC (Flexible Printed Circuit) 206. A clock generator 209 is provided outside the substrate 201. Note that the charge pump comprises the switching element 205 and capacitors 207 and 208.

Please replace the paragraph beginning at page 6, line 5, with the following amended paragraph:

FIG. 1 shows an outline view of a display device of the invention. A display device 101 of the invention has a pixel portion 104, a source signal line driving circuit 102, a gate signal line driving circuit 103, a switching element 105, a variable frequency-dividing circuit 107 and a CPU 108 formed integrally with TFTs on a substrate 101. Also, an FPC 106 has capacitors 109 and 110. The capacitors are loaded on the FPC here, but the place is not limited to the FPC. They can be loaded on the substrate 101 or other substrates such as a glass substrate, a plastic substrate, a stainless substrate, a silicon substrate. A clock generator 111 is provided outside the substrate 101.

Please add the following new paragraph after the paragraph ending at page 6, line 27:

Fig. 6 is a block diagram of a charge pump control circuit of the invention. The charge pump control circuit shown in Fig. 6 is constituted of a clock generator 601, a variable frequency-dividing circuit 602, a CPU 603, a charge pump circuit 604 and a signal line driving circuit 605. A signal from the clock generator 601 is inputted to the variable frequency-dividing circuit 602, and the CPU 603 controls the variable frequency-dividing circuit 602. Then, an output of the variable frequency-dividing circuit 602 is inputted to the charge pump circuit 604, and the charge pump circuit 604 supplies a voltage to the signal line driving circuit 605.